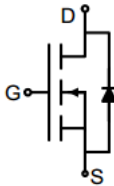
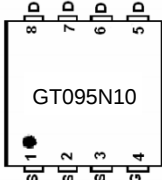
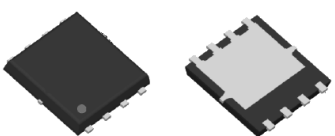


N-Channel Enhancement Mode Power MOSFET

Description The GT095N10D5 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.		 Schematic diagram	
General Features • V_{DS} 100V • I_D (at $V_{GS} = 10V$) 60A • $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 9.5mΩ • $R_{DS(ON)}$ (at $V_{GS} = 4.5V$) < 13mΩ • 100% Avalanche Tested • RoHS Compliant		 Marking and pin assignment	
Application • Synchronous Rectification in SMPS or LED Driver • UPS • Motor Control • BMS • High Frequency Circuit		 DFN5*6	
Device	Package	Marking	Packaging
GT095N10D5	DFN5*6	GT095N10	5000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	100	V
Continuous Drain Current	I_D	60	A
Pulsed Drain Current (note1)	I_{DM}	240	A
Gate-Source Voltage	V_{GSS}	± 20	V
Power Dissipation	P_D	63	W
Single pulse avalanche energy (note3)	E_{AS}	90	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ\text{C}$

Thermal Resistance

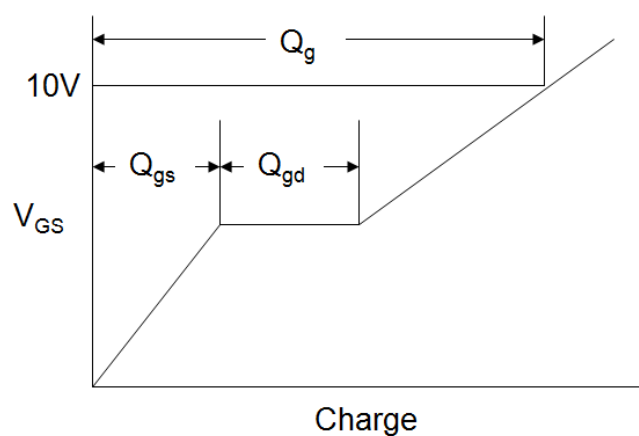
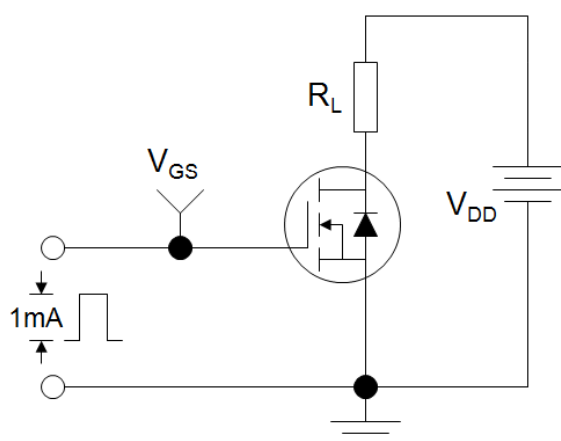
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	2.0	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	55	$^\circ\text{C/W}$

Specifications T _J = 25°C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
Gate-Source Leakage	I _{GSS}	V _{GS} = ±20V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.3	1.8	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 35A	--	9	9.5	mΩ
		V _{GS} = 4.5V, I _D = 35A	--	12.5	13	
Forward Transconductance	g _{FS}	V _{DS} =5V,ID=35A	--	13.5	--	S
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 50V, f = 1.0MHz	--	2122	--	pF
Output Capacitance	C _{oss}		--	618	--	
Reverse Transfer Capacitance	C _{rss}		--	25	--	
Total Gate Charge	Q _g	V _{DD} = 50V, I _D = 20A, V _{GS} = 10V	--	41.8	--	nC
Gate-Source Charge	Q _{gs}		--	9	--	
Gate-Drain Charge	Q _{gd}		--	10	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 50V, I _D = 20A, R _G = 3Ω	--	17	--	ns
Turn-on Rise Time	t _r		--	4	--	
Turn-off Delay Time	t _{d(off)}		--	32	--	
Turn-off Fall Time	t _f		--	8	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25°C	--	--	60	A
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 35A, V _{GS} = 0V	--	--	1.0	V

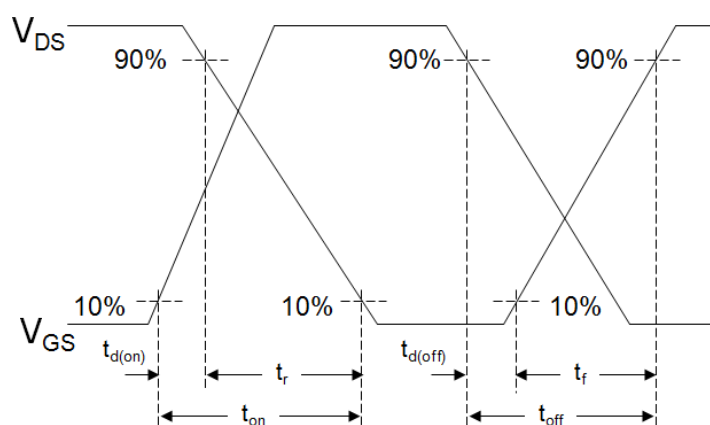
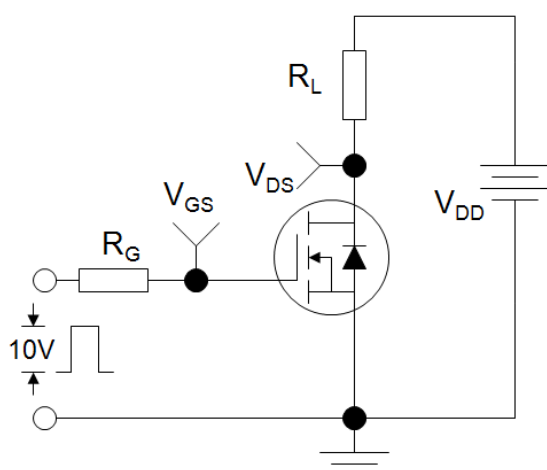
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical R_G
3. EAS condition : $T_J=25, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25\Omega$

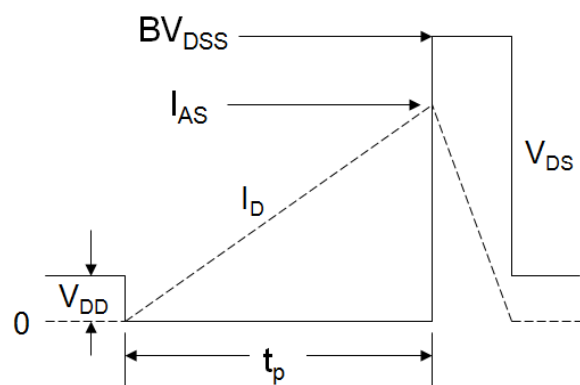
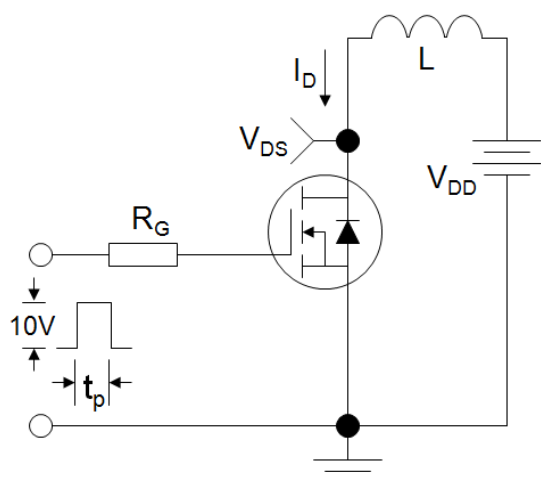
Gate Charge Test Circuit



EAS Test Circuit



Switch Time Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

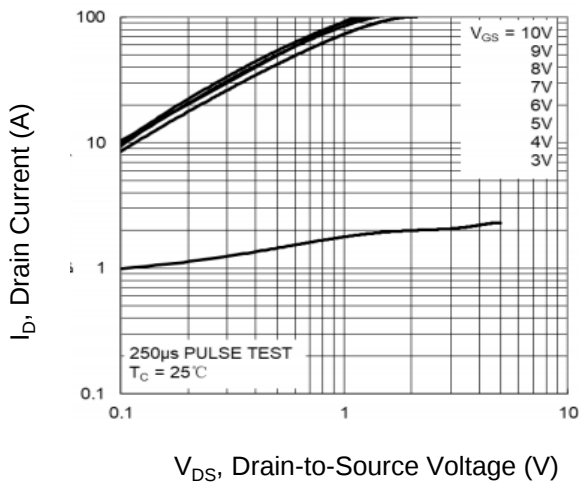


Figure 2. Transfer Characteristics

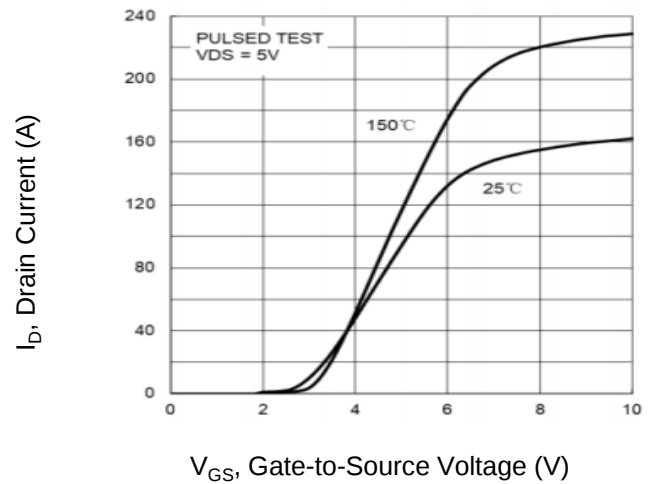


Figure 3. $R_{DS(on)}$ -Drain Current

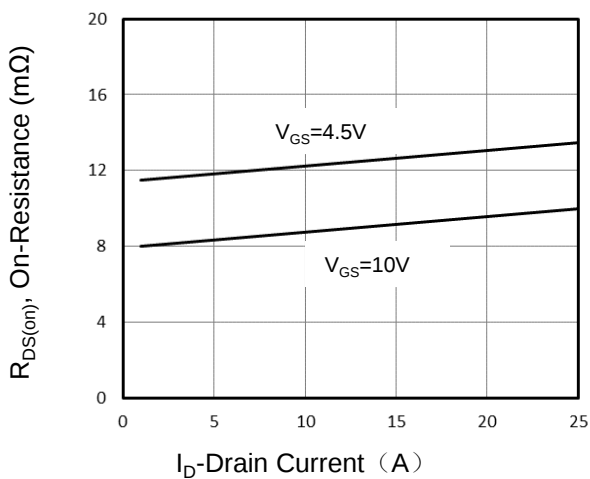


Figure 4. Gate Charge

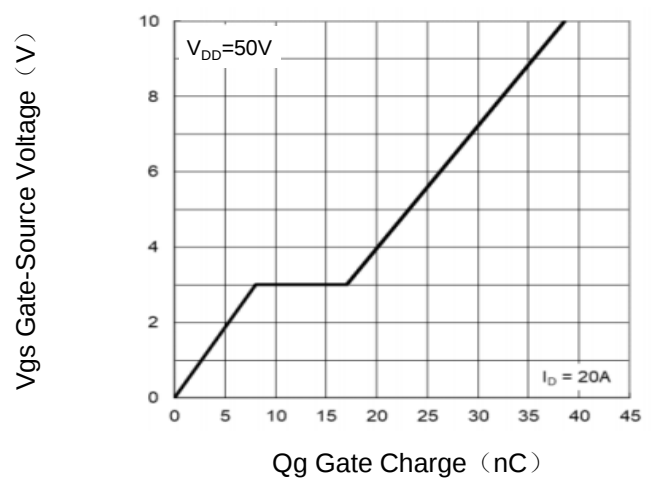


Figure 5. Capacitance vs V_{ds}

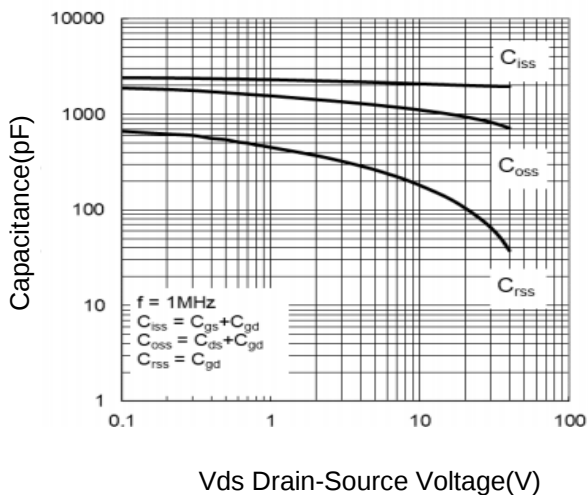
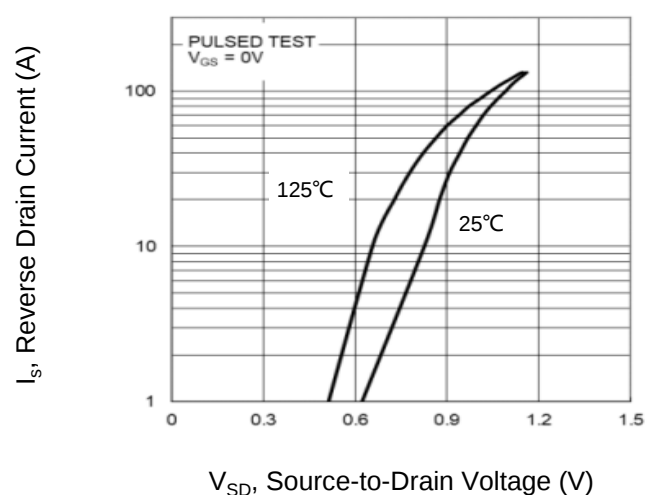


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

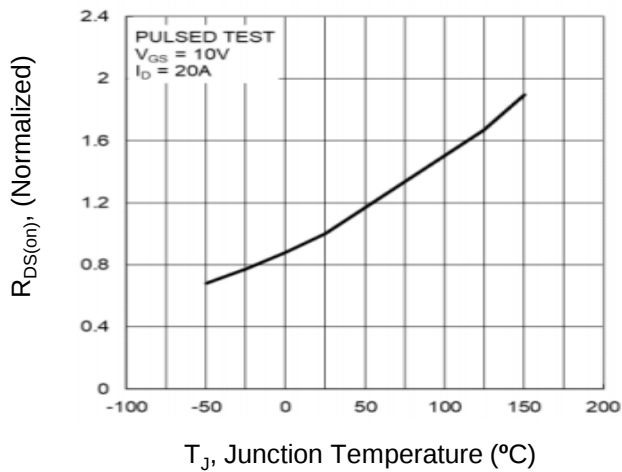


Figure 8. Safe Operation Area

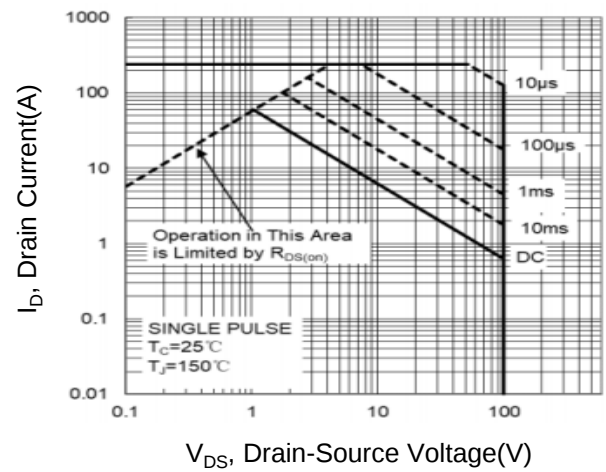
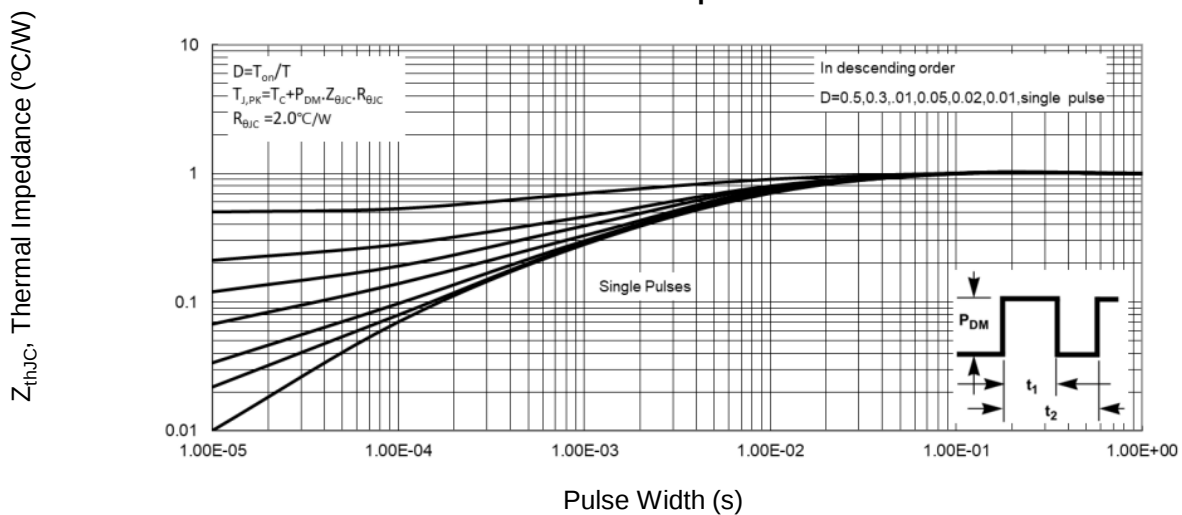
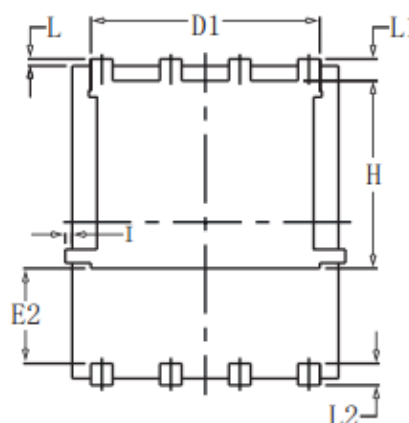
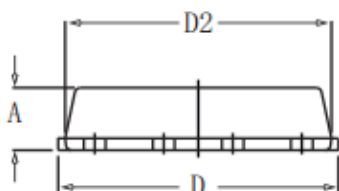
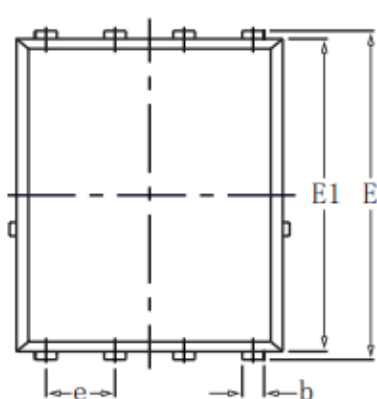


Figure 9. Normalized Maximum Transient Thermal Impedance



DFN5×6-8L Package Information



SYMBOL	COMMON			
	MM		INCH	
	MIN	MAX	MIN	MAX
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.970	0.0324	0.0382
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.59	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	-	0.0630	-
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	-	0.18	-	0.0070